IN THE SPECIFICATION:

Kindly amend Paragraph 26, beginning at page 12, as set forth below:

[0026] The controller 120 of the power converter receives a desired characteristic such as a desired system voltage V_{system} from an internal or external source associated with the microprocessor, and the output voltage V_{out} of the power converter. In accordance with the aforementioned characteristics, the controller 120 provides a signal (*e.g.*, a pulse width modulated signal S_{PWM}) to control a duty cycle and a frequency of the main and auxiliary switches Q_{mn}, Q_{aux} of the power train 110 to regulate the output voltage V_{out} thereof. Any controller adapted to control at least one switch of the power converter is well within the broad scope of the present invention. As an example, a controller employing digital circuitry is disclosed in U.S. Patent Application Publication No. 2005/0169024~~Serial No. [Attorney Docket No. ENP-001]~~, entitled "Controller for a Power Converter and a Method of Controlling a Switch Thereof," to Dwarakanath, *et al.* and U.S. Patent Application Publication No. 2005/0168205 ~~Serial No. [Attorney Docket No. ENP-002]~~, entitled "Controller for a Power Converter and Method of Controlling a Switch Thereof," to Dwarakanath, *et al.*, which are incorporated herein by reference.

Kindly amend Paragraph 27, on page 13, as set forth below:

[0027] The power converter also includes the driver 130 configured to provide drive signals S_{DRV1}, S_{DRV2} to the main and auxiliary switches Q_{mn}, Q_{aux}, respectively, based on the signal S_{PWM} provided by the controller 120. There are a number of viable alternatives to implement a driver 130 that include techniques to provide sufficient signal delays to prevent crosscurrents when controlling multiple switches in the power converter. The driver 130 typically includes switching circuitry incorporating a plurality of driver switches that cooperate to provide the drive signals S_{DRV1}, S_{DRV2} to the main and auxiliary switches Q_{mn}, Q_{aux}. Of course, any driver 130 capable of providing the drive signals S_{DRV1}, S_{DRV2} to control a switch is well within the broad scope of the present invention. Additionally, an embodiment of a driver is disclosed in U.S. Patent Application Publication No. 2005/0168203~~Serial No. [Attorney Docket No. ENP-003]~~, entitled "Driver for a Power Converter and Method of Driving a Switch Thereof," to Dwarakanath, *et al.*, which is incorporated herein by reference.